



THE RESONANCE

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

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Suman
3LA22EC040
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3LA22EC041

EMERGING TRENDS ON CLOUD COMPUTING— AWS

Date: 9th & 10Th February 2024

Chief Guest

Mr. Praveenkumar Eklare

Lead Administrator
Wipro Pvt. Ltd.
Hyderabad

Introduction to Cloud Computing & AWS.

Hands-on Training : EC2 Instances, Load Balancers, Auto-Scaling

Managing Volumes, Snapshots and Troubleshooting EC2

Setting Up AWS Account

Practical Experience with AWS Services : S3, IAM and VPC

Management & Monitoring Tools: Cloud Watch, Cloud Trail

Real-World Use Cases & Best Practices

AWS stands for Amazon Web Services. It is a cloud computing platform offered by Amazon that provides IT resources and services through the internet.

Instead of purchasing physical computers, servers, and storage devices, organizations can rent computing resources from AWS whenever required.



Inside this issue:

NEXT GENERATION VLSI ARCHITECTURE AND METHODOLOGIES

Emerging Trends on Cloud Computing— AWS 1

Next Generation VLSI Architecture and Methodologies 1

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Mr. Prashant Kumar Gummal, a VLSI veteran with over 13 years of profound experience in ASIC/SoC/IP Verification. Currently he is working as SMTS engineer in AMD pvt ltd, Hyderabad and leading engineering team of size 10-15 engineers.

Prashant's journey into this highly specialized field was underpinned by a strong academic record, including a Master of Technology in VLSI & ES Design from Bangalore Institute of Technology. This strong foundation has enabled him to excel in a career marked by his compressive command over the entire verification lifecycle, from developing advanced UVM-based environments and crafting .

STUDENT'S ACHIEVEMENTS



**LINGARAJ APPA
ENGINEERING COLLEGE, BIDAR**

DEPARTMENT OF
ELECTRONICS & COMMUNICATION ENGINEERING

4th SEM TOPPERS

*** ACADEMIC YEAR 2023-24 ***

1st



DIVYALAXMI
USN: 3LA22EC013
84.89%

2nd



VINAYASHREE
USN: 3LA22EC045
83.44%

3rd



BIRADAR TRIVENI
USN: 3LA22EC012
83.33%

Hearty Congratulations!
Your hard work, dedication and perseverance
have brought glory to the department.
Keep shining and achieving greater heights!

SECRETARY | PRINCIPAL | HOD | & ALL TEACHING & NON-TEACHING STAFF MEMBERS
*** TOGETHER WE CELEBRATE SUCCESS! ***



**LINGARAJ APPA
ENGINEERING COLLEGE, BIDAR**

DEPARTMENT OF
ELECTRONICS & COMMUNICATION ENGINEERING

6th SEM TOPPERS

*** ACADEMIC YEAR 2023-24 ***

1st



ADINATH
USN: 3LA21EC001
85.13%

2nd



DIVYA
USN: 3LA21EC006
84.88%

3rd



SUDHAKAR
USN: 3LA21EC028
82.75%

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**LINGARAJ APPA
ENGINEERING COLLEGE, BIDAR**

DEPARTMENT OF
ELECTRONICS & COMMUNICATION ENGINEERING

8th SEM TOPPERS

*** ACADEMIC YEAR 2023-24 ***

1st



ASHWINI
USN: 3LA20EC002
93%

2nd



AKSHATA
USN: 3LA20EC001
91%

3rd



MADHUKESHAV
USN: 3LA20EC004
90%

Hearty Congratulations!
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Toppers of Even semester(2023-24)

Annexure I

KARNATAKA STATE COUNCIL FOR SCIENCE AND TECHNOLOGY
Indian Institute of Science Campus, Bengaluru - 560012

**LIST OF PROJECTS SANCTIONED UNDER
47th SERIES OF STUDENT PROJECTS PROGRAMME: 2023 – 2024**

820.	47S_BE_2349	DEEP LEARNING BASED MULTI RETINAL DISEASES CLASSIFICATION	B.E.	ELECTRONICS AND COMMUNICATIO N ENGINEERING	Dr. SHARANBASAPP A SHETKAR	Mr. SHRISHAIL Mr. DAYANAND
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MAHARISHI VALMIKI JAYANTI



Major objective of celebration to aware teachers and students about Maharshi Valmiki. Pragat Diwas or Valmiki Jayanti is a festival that celebrates the birth anniversary of Maharishi Valmiki. He was a renowned saint poet who is celebrated for writing the Hindu epic Ramayana. The people observing the day often commemorate it by chanting Ramayana lyrics at Valmiki temples around the country.

RECENT TRENDS IN VLSI TESTING



Date: 13/07/2024

Guest: Mr. Lokesh Patil

Objectives of the Event

Keeping Up with Technological Advances

Enhancing Test Coverage and Quality

Reducing Test Costs

Improving Fault Diagnosis and Yield

Addressing New Challenges

Enhancing Power Efficiency and Performance

Complying with Industry Standards

Facilitating Design for Testability

Adopting Machine Learning and AI Networking with Industry Experts Career Advancement

Outcomes of the Event

Advanced Knowledge and Expertise:

Gain a deep understanding of the latest trends, techniques, and methodologies in VLSI testing.

Acquire specialized knowledge in areas like fault diagnosis, Design for Testability (DFT), and test optimization.

Improved Test Coverage and Quality:

Develop the ability to implement testing strategies that ensure higher test coverage and improved quality of VLSI circuits.

ANTENNAS: BASICS TO SMART



Dr. Mohammed Bakhar is Professor in Department of E&CE, Guru Nanak Dev Engineering College, Bidar with 26 years of teaching, research & innovation experience. He obtained M.E in Communication systems and Ph.D in Smart Antennas from Gulbarga University. His expertises are in Digital Communication, Electromagnetic Theory, Antenna theory, Microwaves and Satellite Communication. He has guided **8** PhDs, **50** UG, **12** PG projects and two patents published. He has received Rs **5+ lakhs** R&D grants and has **35** technical publications with **249** citations having 9 h- index & 7-i-10 index. He has worked as a session chair in various conferences in college and College of Engineering, Pandharpur. He has organized 10 faculty/student development programs. Has organized FDP on “**Antenna Technologies and Smart Antenna**” sponsored (**2 lakhs**) by Vision Group of Science and Technology, Govt. of Karnataka, Bangalore. Has attended 20+ programmes and delivered more than 3 invited talks. He has worked as member of VTU LIC committee and Faculty selection. He is life member of ISTE and fellow of IETE.

INVITATION

You are cordially invited for technical talk on

"Antennas: Basics to Smart Evolution"

By

IEEE Student Branch, LAEC, Bidar



On

28th June 2024 at 11.00 am

CHIEF PATRON

Parama Poojya Dr. Sharanbasappa Appa
Mahadad Patil, Sharanbasappa Smadana, Kalaburagi
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President, Akhila Bharata Anubhava Mantapa Kalaburagi,
Chancellor Shambhava University, Kalaburagi

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Shri Basavaraj Deshmukh
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Kalaburagi

PATRON

Dr. Umatal Deshmukh
Director
Lingarajappa Engineering College
Bidar

PATRON

Shri Sharanabasappa Deshmukh
Director
Lingarajappa Engineering College
Bidar

Keynote Speaker

Dr. Mohammed Bakhar
Professor, Department of E&CE,
Guru Nanak Dev Engineering College,
Bidar

Gornalli, Bidar 585403
Karnataka



Department Vision

The ECE department aims to lead the way in research and higher education while developing innovative solutions to societal problems.

Department Mission

M1: A Centre of excellence for technology, training, teaching and research with the goal of producing elite electronics and communication engineers to benefit society and the country.

M2: To instill a spirit of entrepreneurship, professional ethics and values that meet the needs of society and industry.

M3: To build a technical workforce by regularly planning workshops, expert talks and Industrial Visits

M4: To facilitate students with opportunities to hone their ethics & leader-

<https://www.lingaraj-appaec.in/electronics-and-communication-engineering-2/>

ABOUT DEPARTMENT

The Department of Electronics and Communication Engineering was established in the Institution during 2011. It is one of the Prestigious and Core department in the Institution, offering a UG program with an intake of 54 students and PG program in VLSI Design and Embedded Systems with an intake of 16. It is also a recognized "Research Center" by VTU. The department is headed by Dr. Sharanbasappa Shetkar. The department is strengthened with 3 Professors, 2 Associate Professors, 7 Assistant Professors, and 2 Technical staff. The department has well-equipped laboratories with state-of-art infrastructure, a department library, and internet facilities. The Department endows with additional industrial training, live projects & hands-on to enhance employability.

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